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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/532,352	03/21/2000	Kogo Endo	9319S-000127	7425

7590 02/22/2006

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EXAMINER

LESPERANCE, JEAN E

ART UNIT PAPER NUMBER

2674

DATE MAILED: 02/22/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/532,352

Applicant(s)

ENDO ET AL.

Examiner

Jean E Lesperance

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 December 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 39-49 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 39-45 is/are allowed.
- 6) ☒ Claim(s) 46 is/are rejected.
- 7) ☒ Claim(s) 47-49 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on September 14, 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____
- 4) ☐ Interview Summary (PTO-413) Paper No(s) _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

1. The amendment filed on December 5, 2005 is entered and claims 39-49 are pending.

Response to Arguments

2. Applicant's arguments with respect to claims 39-49 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 46 is rejected under 35 USC 102 (a) as being unpatentable over US Patent # 5,467,210 ("Kishigami").

Regarding claim 3, Kishigami teaches a display device having a display panel (an LCD device (column 1, line 9)) including:

a first glass substrate (7a) opposing to the second glass substrate 6a (Fig.9), the second substrate (6a) including an extended area that projects beyond a peripheral edge of the first glass substrate (7a);

an electrooptic material layer interposed between the substrates (an LCD device (column 1, lines 9) which is inherently include an electrooptic material layer;

an input line disposed on the extended area of the second substrate (input line coming from the integrated circuit 3a on the extended area);

an integrated circuit mechanically fixed on the second glass substrate on the extended portion (ic or integrated circuit 3a is mounted on the second glass substrate 7a);

a flexible circuit board disposed on the opposite side of the integrated circuit from the second glass substrate and electrically connected to the integrated circuit (a flexible connected (4) is on the opposite side of the integrated circuit and is connected to the integrated circuit (see Fig.9);

an electronic component mounted on the flexible circuit board at a position the overlaps the integrated circuit plane view (wiring patterns (4b) are electronic component mounted on the flexible circuit board and if looking at the edge position would overlaps the integrated circuit plane view);

an insulating substrate interposed between the integrated circuit and the flexible circuit board (bonding methods can be employed in which the bumps of the electrodes of the IC chips 21 and 22 are in direct contact with the corresponding pads and the IC chips 21 and 22 are bonded to the LCD device 11 using an ultraviolet hardened insulating adhesive and in which the upper surfaces of the pads of ITO or the like are plated with gold and the bumps of a solder of the IC chips 21 and 22 are directly bonded

to the pads. In FIG. 4, a seal between the glass substrates 12 and 13 is denoted at 45 (column 5, lines 60-67)).

Allowable Subject Matter

5. Claims 47-49 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

6. Claims 39-45 are allowed.

7. The following is an examiner's statement of reasons for allowance: the claimed invention is directed to a display device.

Independent claim 39 identifies a uniquely distinct feature "a flexible circuit board substantially accommodated within the extended portion in plan view and mounted with an electronic component that overlaps the data signal driver integrated circuit in plan view, the flexible circuit board having an opposing side that at least partially opposes the data signal driver integrated circuit and a signal output terminal provided adjacent an edge of the opposing side, the signal output terminal being electrically connected to the input line at a position of the extended area between one of the short sides of the extended area and the mounting position of the data signal driver integrated circuit".

Independent claim 39 identifies a uniquely distinct feature "a flexible circuit board substantially accommodated within the second extended portion in plan view and mounted with an electronic component that overlaps the data signal driver integrated circuit in plan view, the flexible circuit board having a signal output terminal adjacent

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one lengthwise edge and a scanning output terminal adjacent the opposite lengthwise edge, the flexible circuit board having an opposing side that at least partially opposes the data signal driver integrated circuit, the signal output terminal being provided at the opposing side, the signal output terminal being electrically connected to the input line at a position of the second extended area between one of the short sides of the second extended area and the mounting position of the data signal driver integrated circuit, the scanning output terminal being electrically connected to the scanning driver integrated circuit".

The closest arts, Sugimoto et al and Kishigami as discussed above, either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

Conclusion

8. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

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the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean Lesperance whose telephone number is (571) 272-7692. The examiner can normally be reached on from Monday to Friday between 10:00AM and 6:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Patrick Edouard, can be reached on (571) 272-7603.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

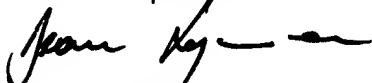
or faxed to:

(571) 273-8300 (for Technology Center 2600 only)

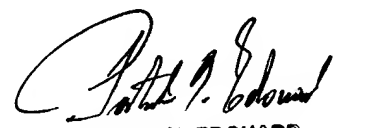
Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Jean Lesperance


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Date 2/13/2006


PATRICK N. EDOUARD
SUPERVISORY PATENT EXAMINER